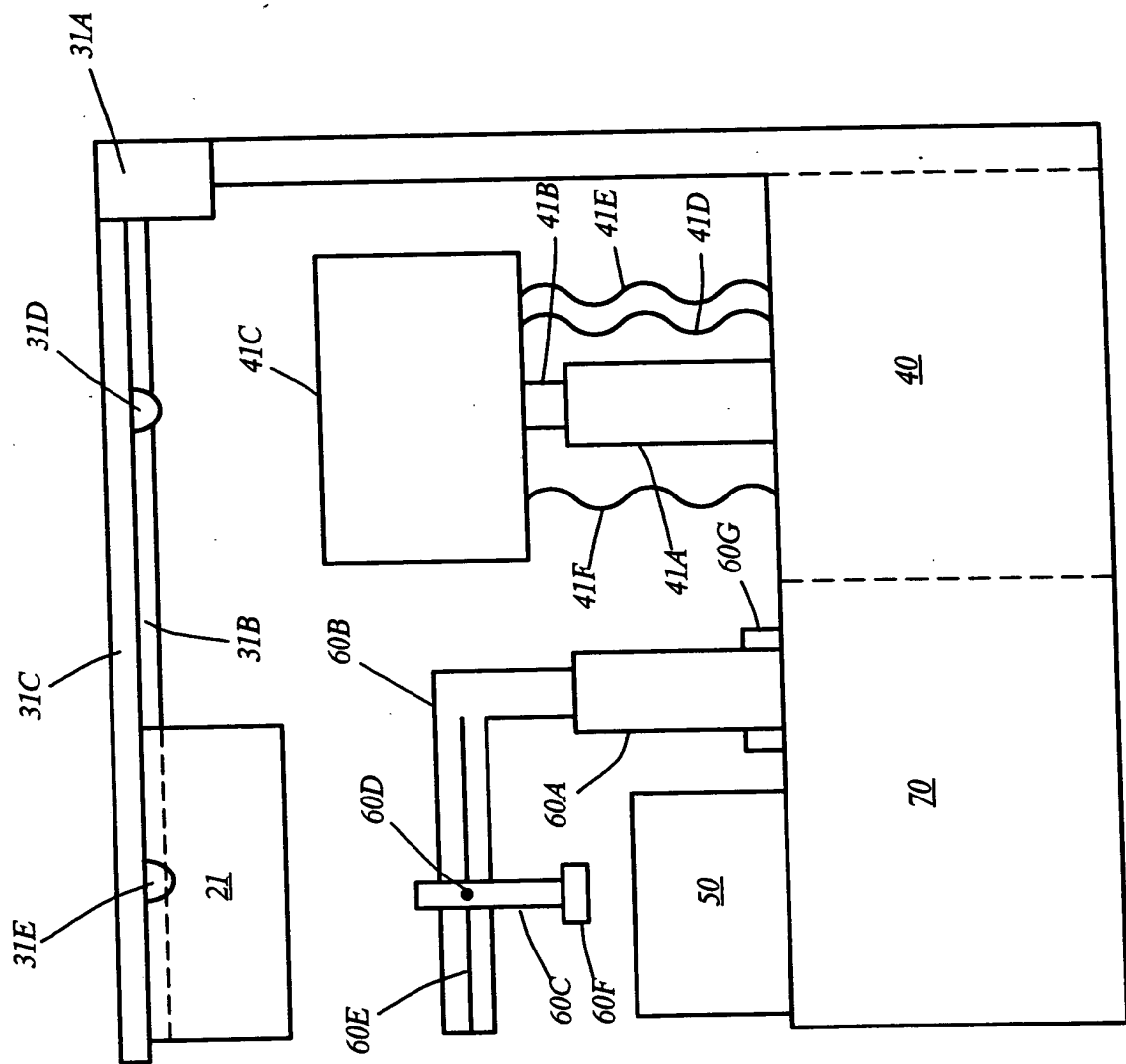
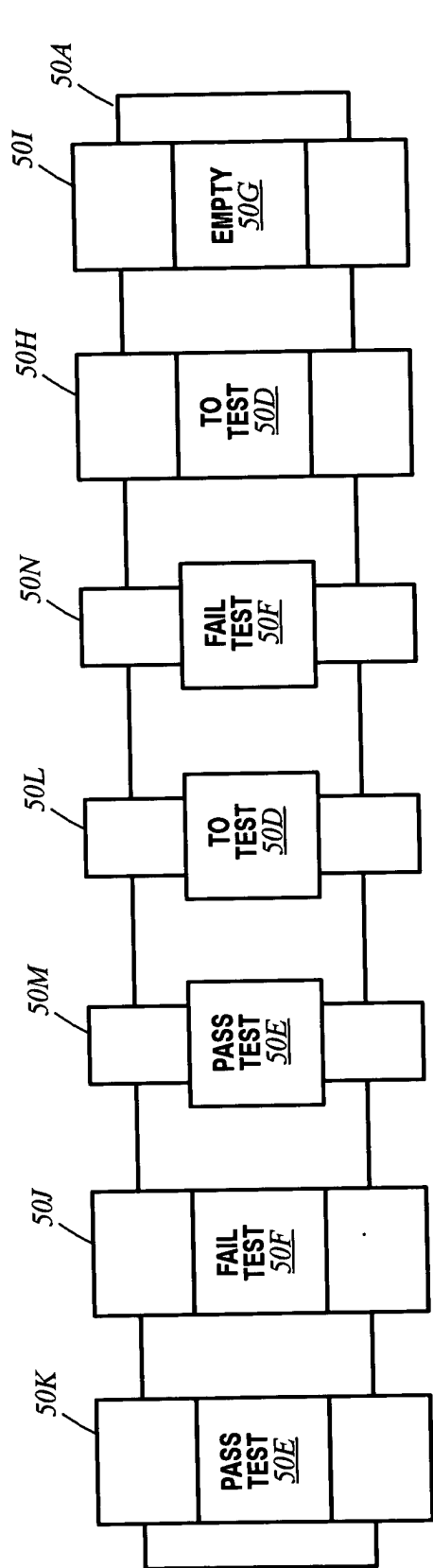


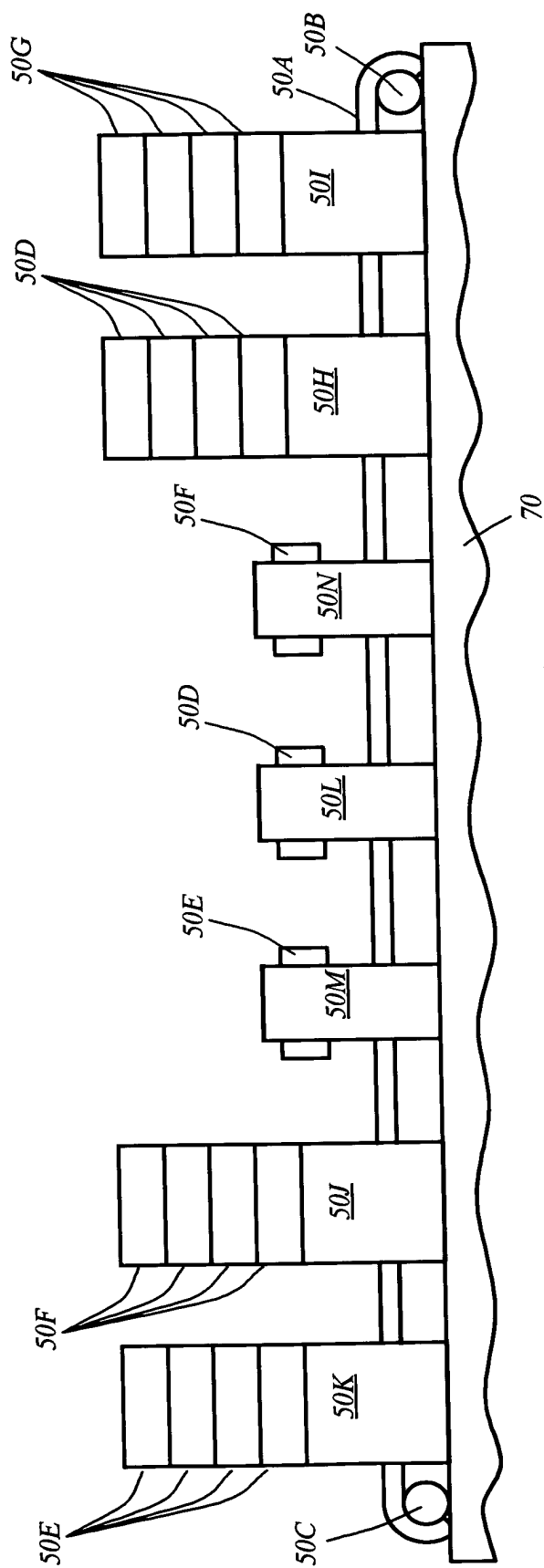
**Figure 1**



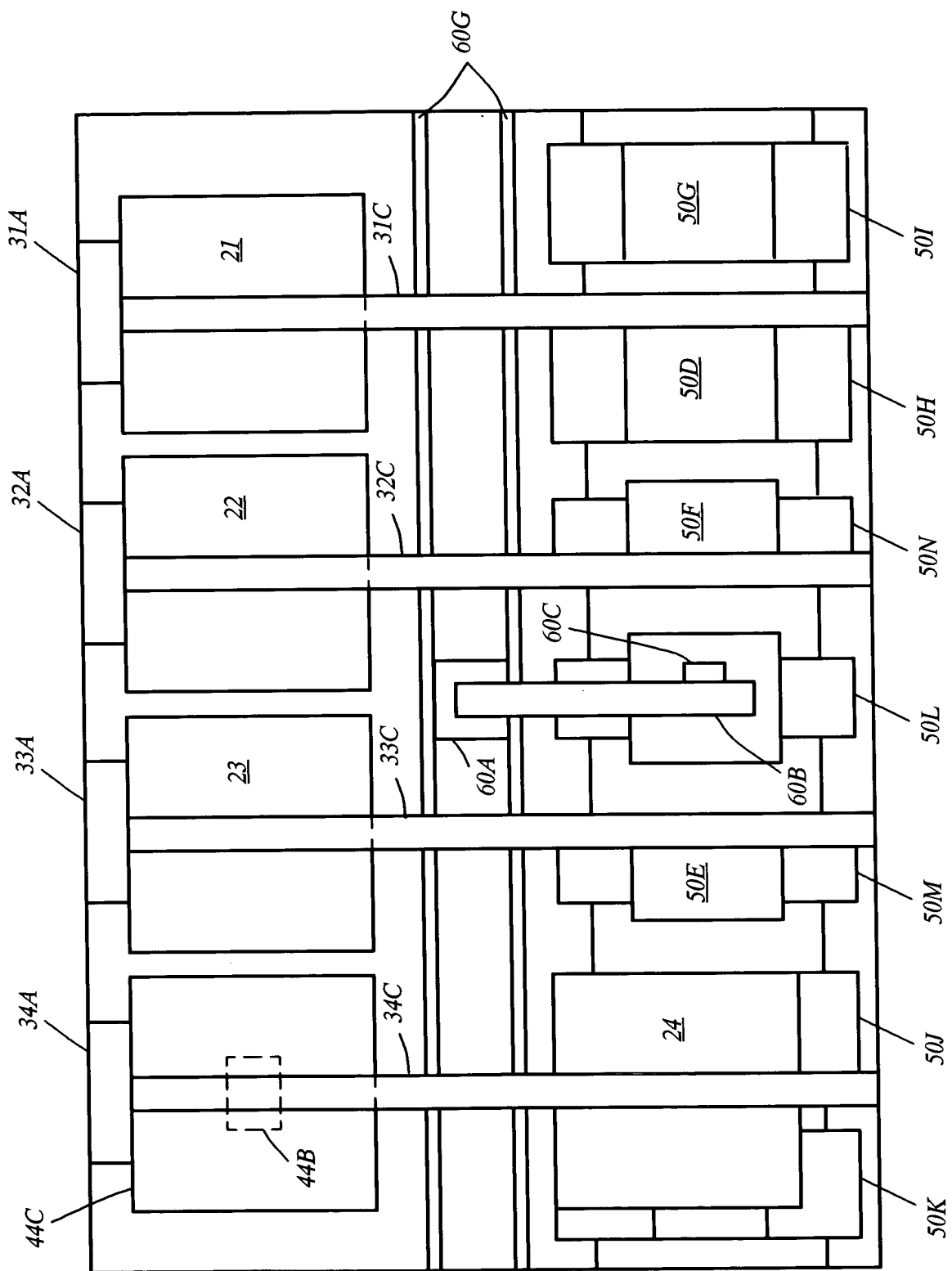
**Figure 2**



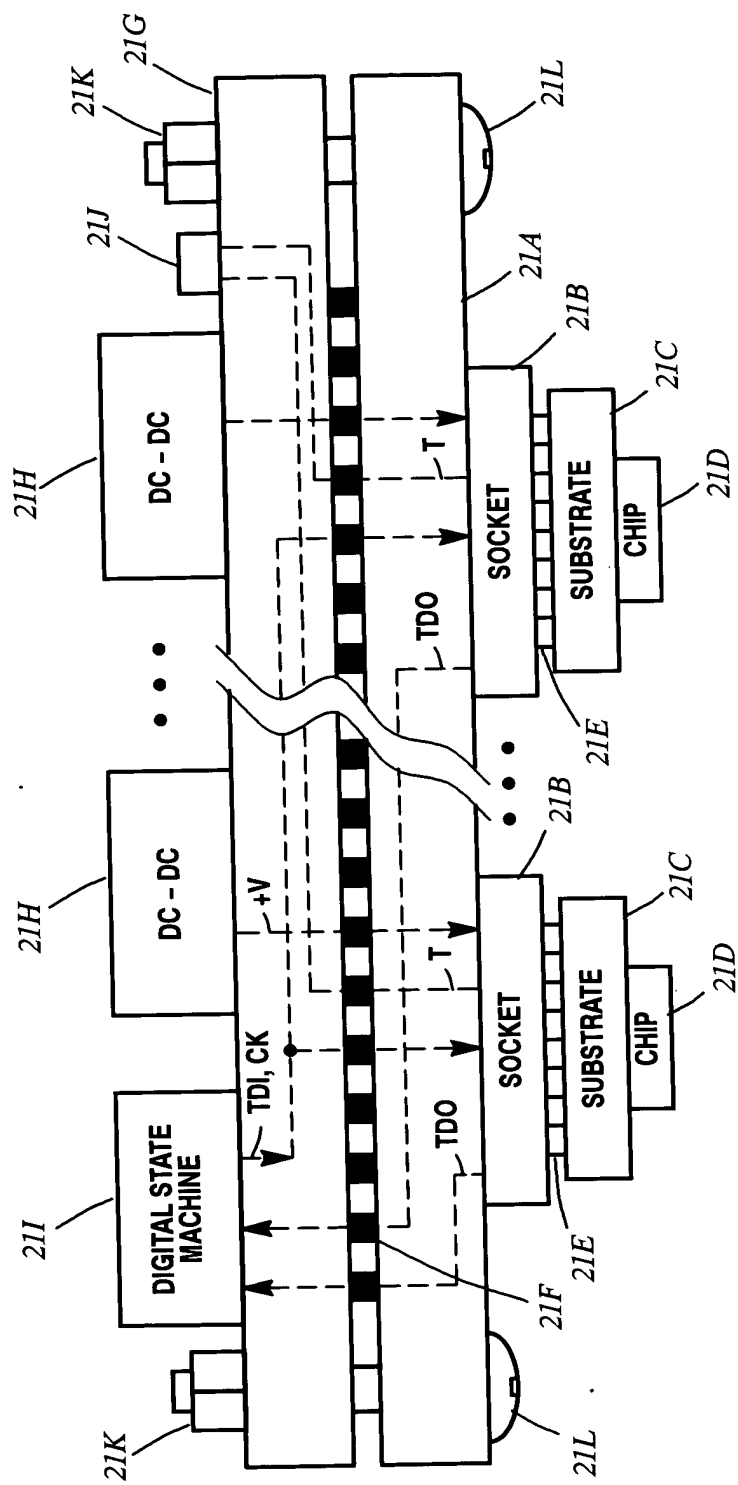
**Figure 3A**



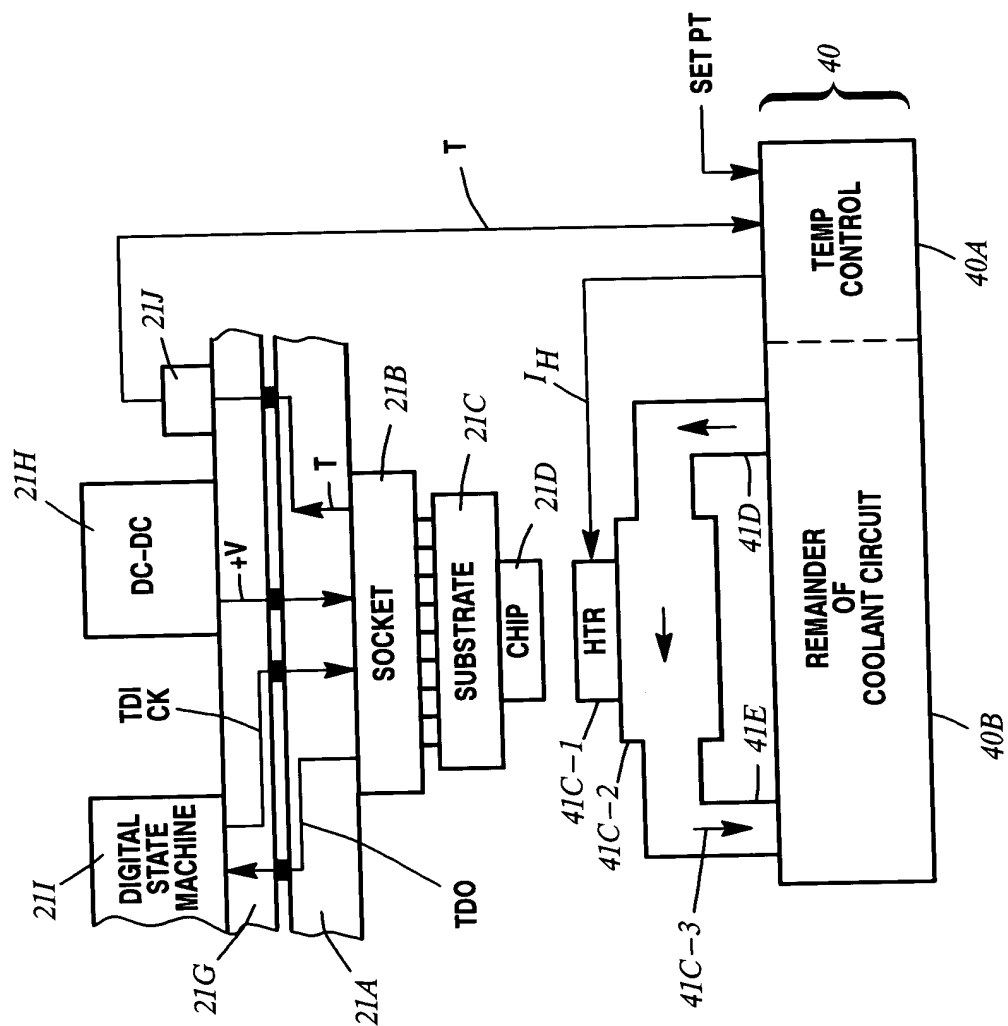
**Figure 3B**



**Figure 4**



**Figure 5**



### Figure 6




	24	23	22	21
t1				LOAD
t2				TEST TO P1
t3			LOAD	TEST TO P2
t4			TEST TO P1	TEST TO P3
t5		LOAD	TEST TO P2	TEST TO P4
t6		TEST TO P1	TEST TO P3	TEST TO P5
t7	LOAD	TEST TO P2	TEST TO P4	TEST TO P6
t8	TEST TO P1	TEST TO P3	TEST TO P5	TEST TO END

**Figure 7A**

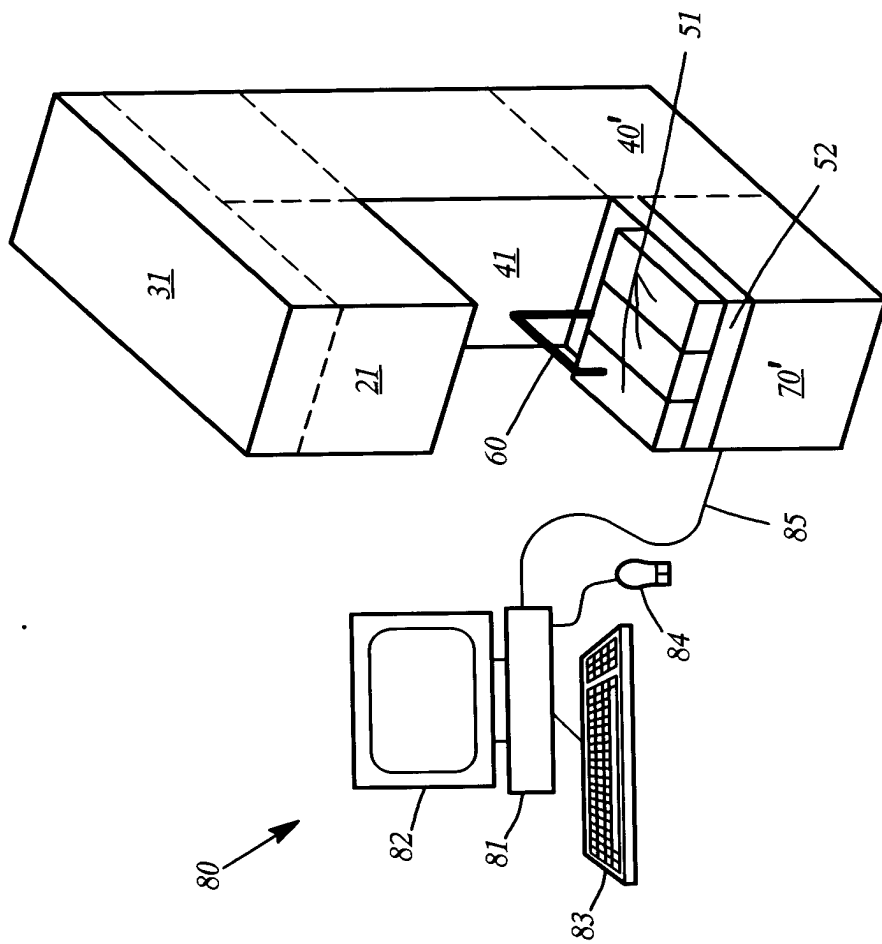
	24	23	22	21
t9	TEST TO P2	TEST TO P4	TEST TO P6	UNLOAD LOAD
t10	TEST TO P3	TEST TO P5	TEST TO END	TEST TO P1
t11	TEST TO P4	TEST TO P6	UNLOAD LOAD	TEST TO P2
t12	TEST TO P5	TEST TO END	TEST TO P1	TEST TO P3
t13	TEST TO P6	UNLOAD LOAD	TEST TO P2	TEST TO P4
t14	TEST TO END	TEST TO P1	TEST TO P3	TEST TO P5
t15	UNLOAD LOAD	TEST TO P2	TEST TO P4	TEST TO P6
t16	TEST TO P1	TEST TO P3	TEST TO P5	TEST TO END

**Figure 7B**

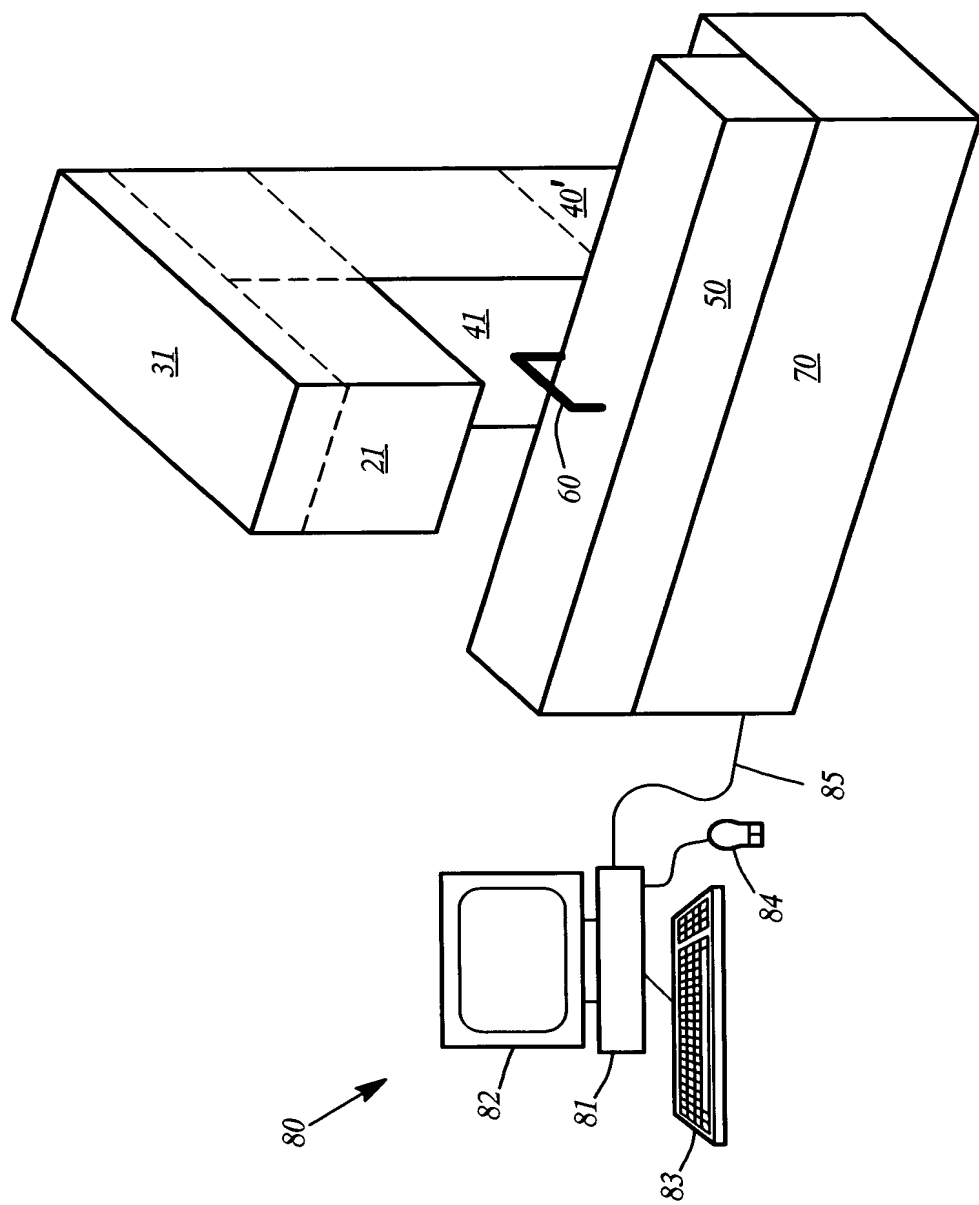


	24	23	22	21
t21	TEST	TEST	TEST	TEST
t22	UNLOAD LOAD	TEST	TEST	
t23	TEST		TEST	UNLOAD LOAD
t24	TEST	UNLOAD LOAD	TEST	TEST
t25	TEST	TEST	TEST	TEST
t26		TEST	UNLOAD LOAD	TEST

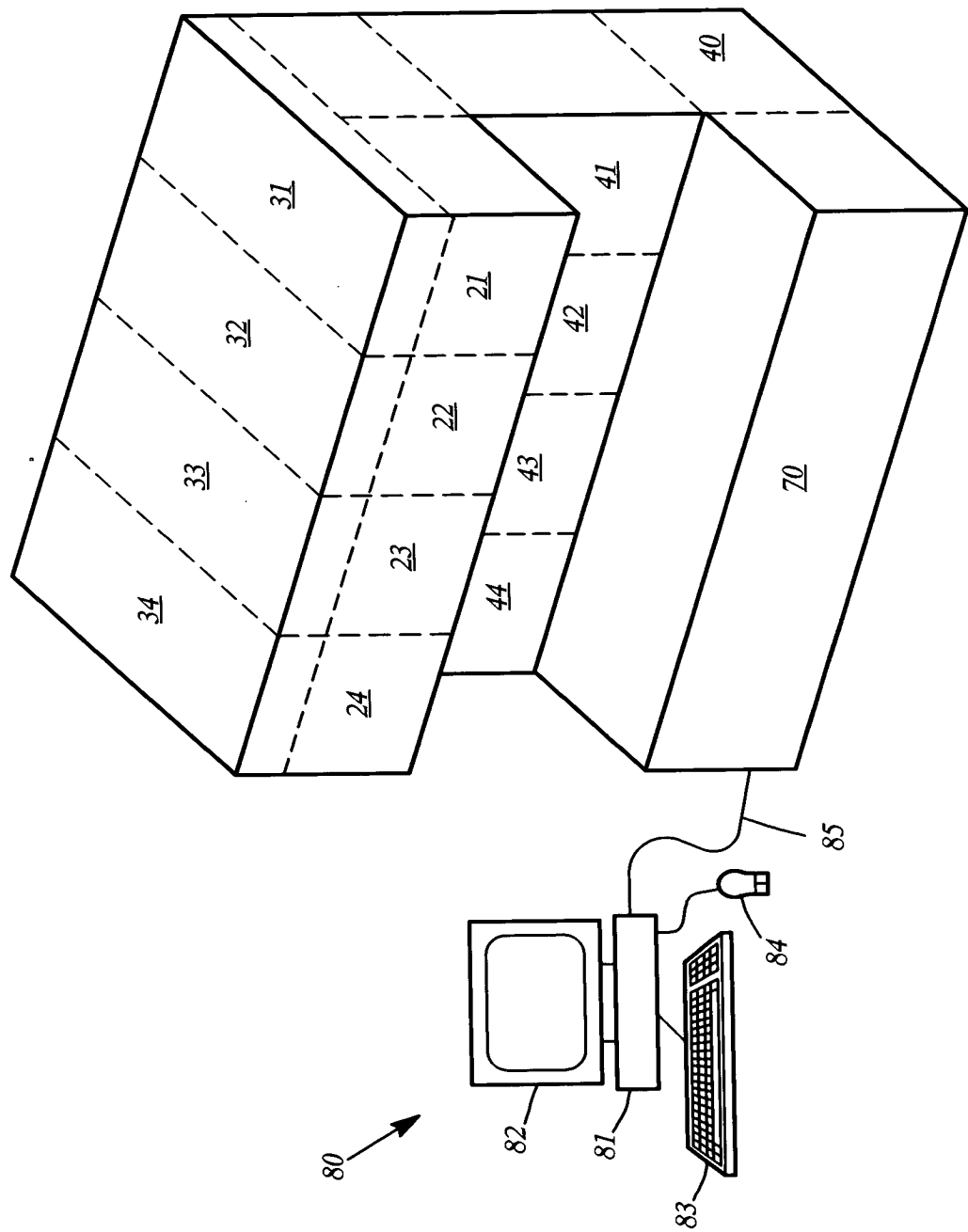
**Figure 8**



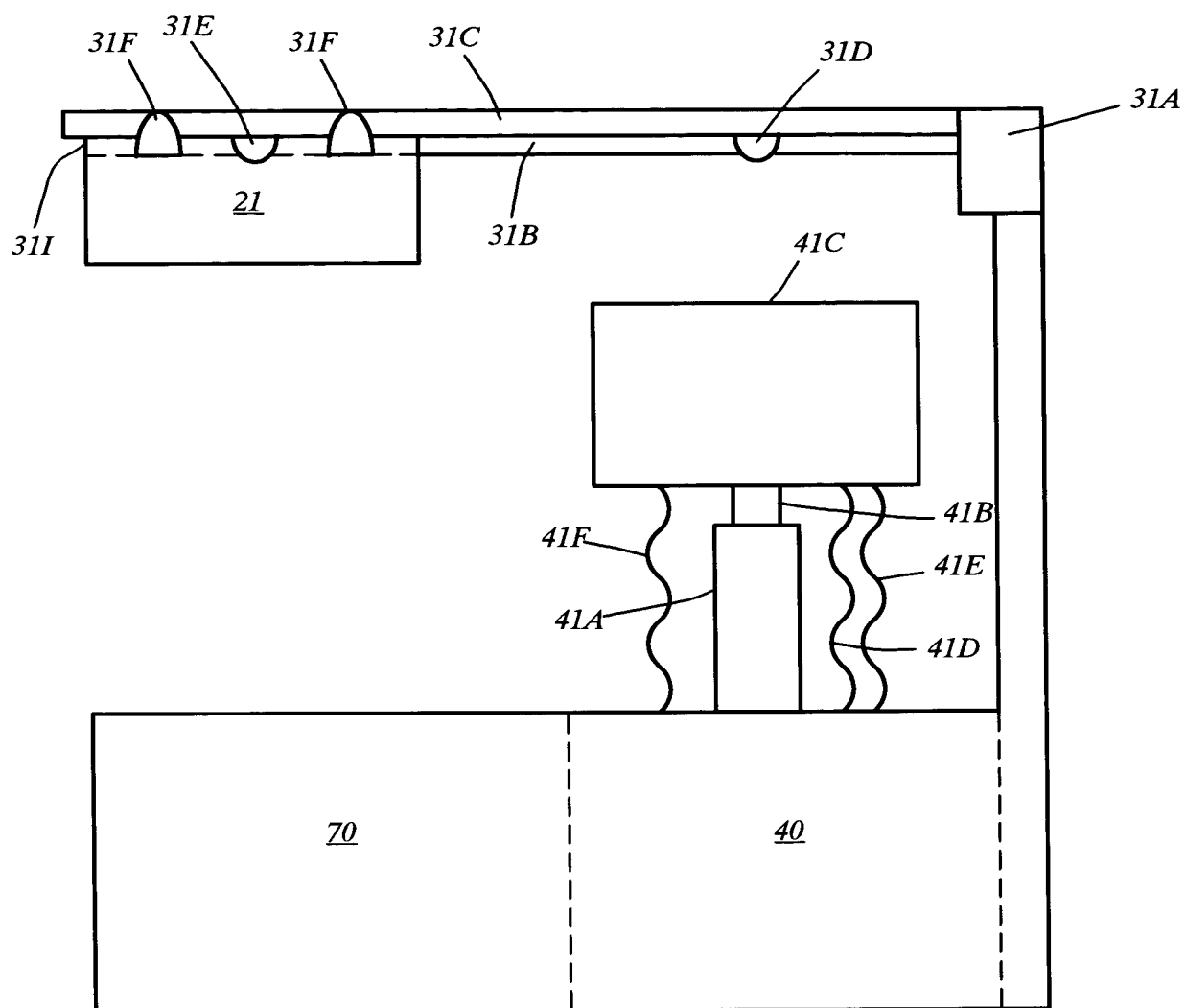
**Figure 9**



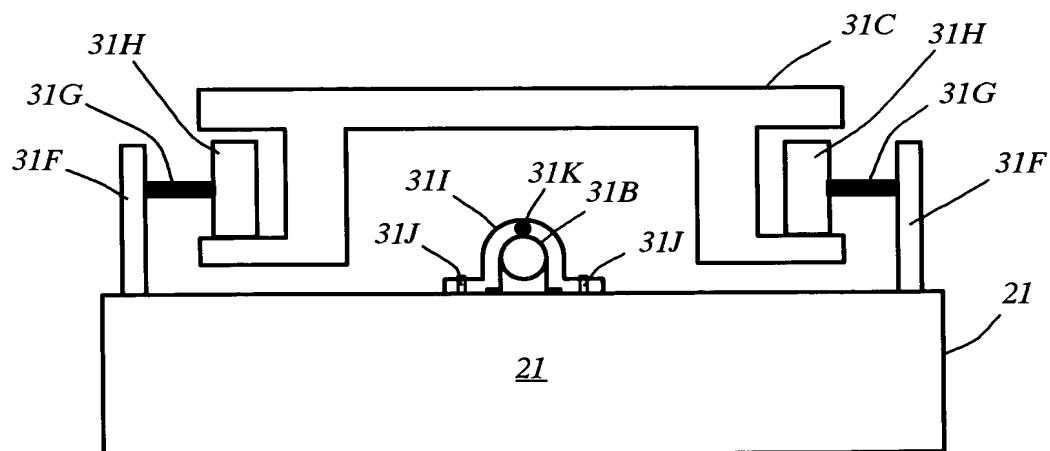
**Figure 10**



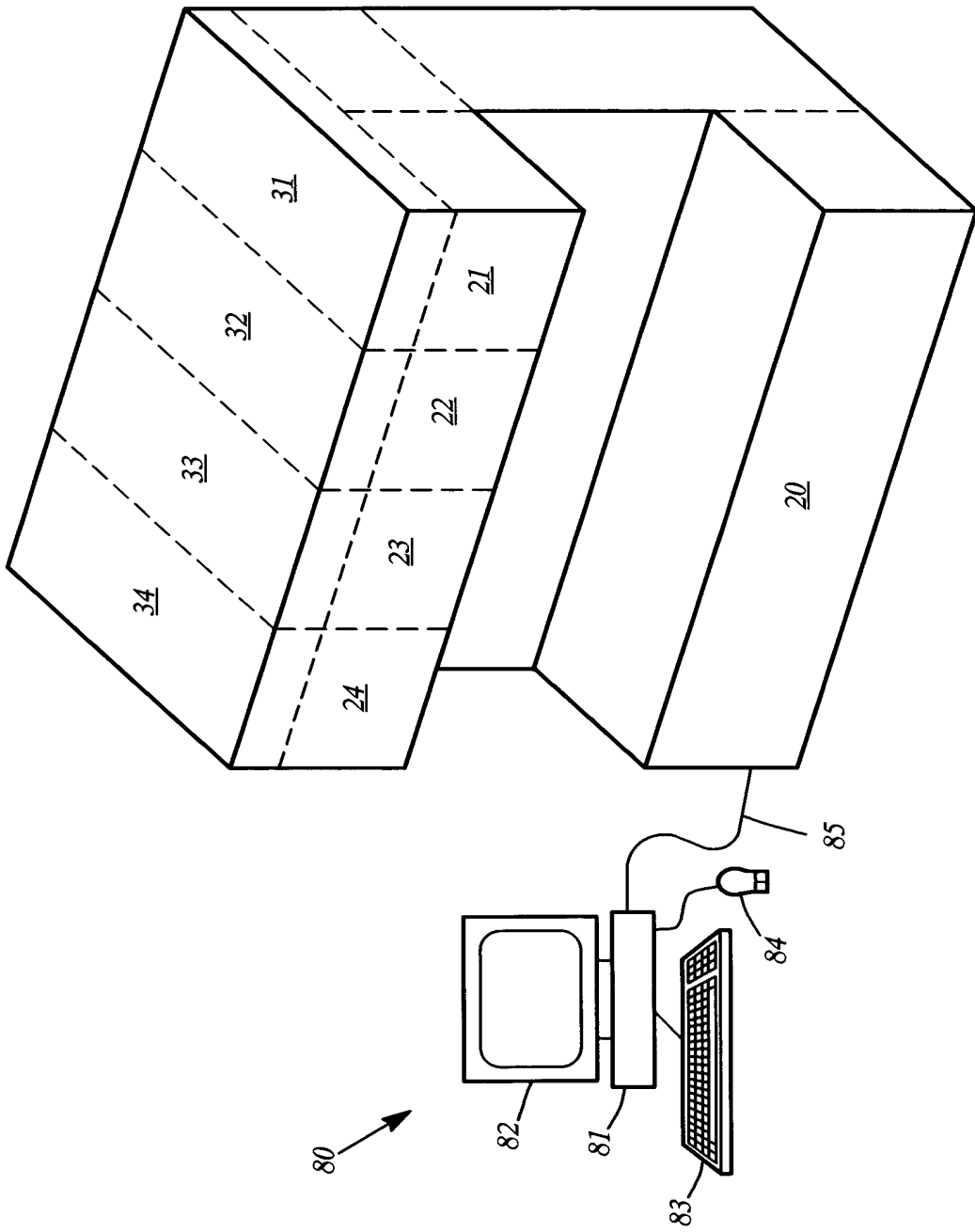
**Figure 11**



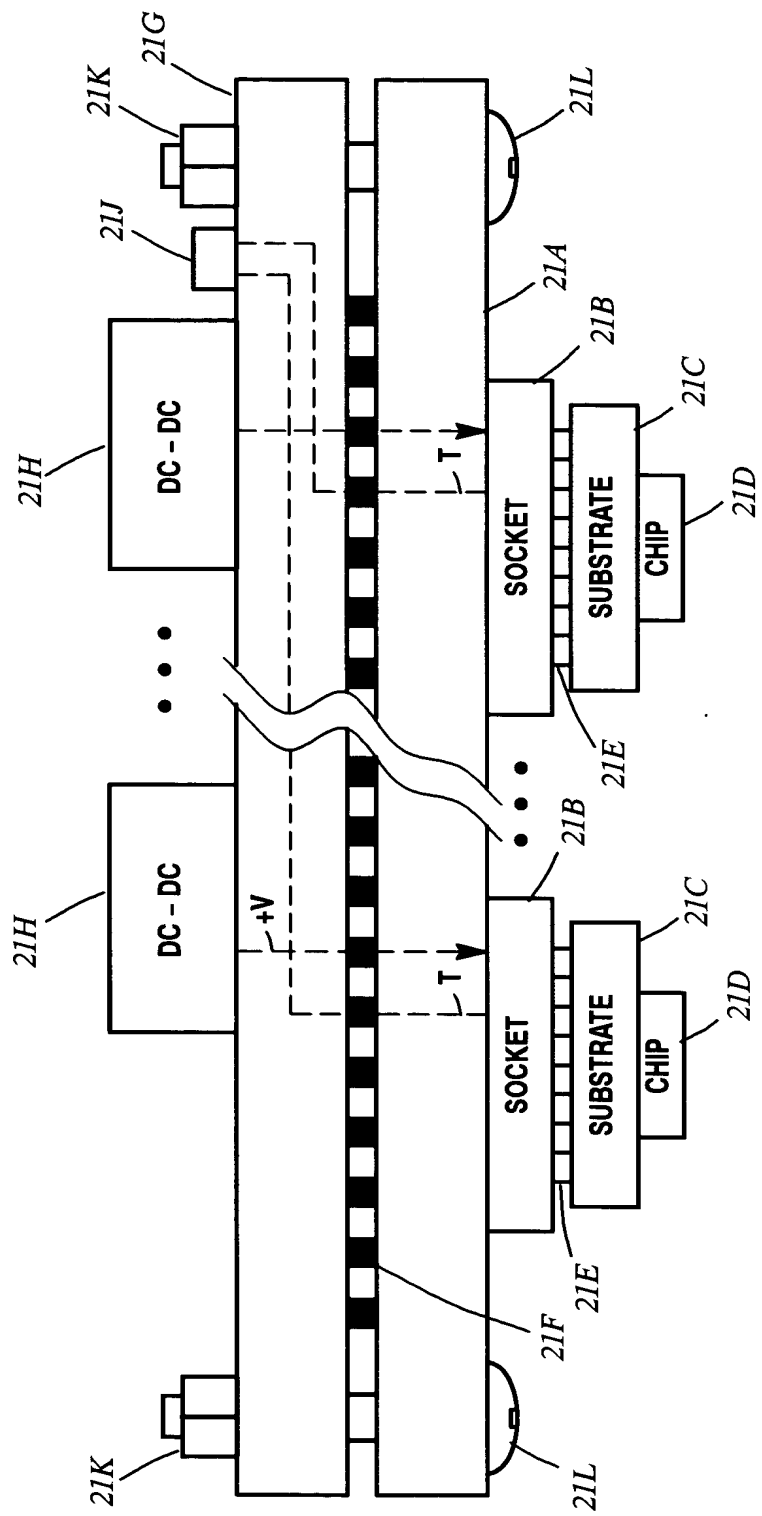
**Figure 12**



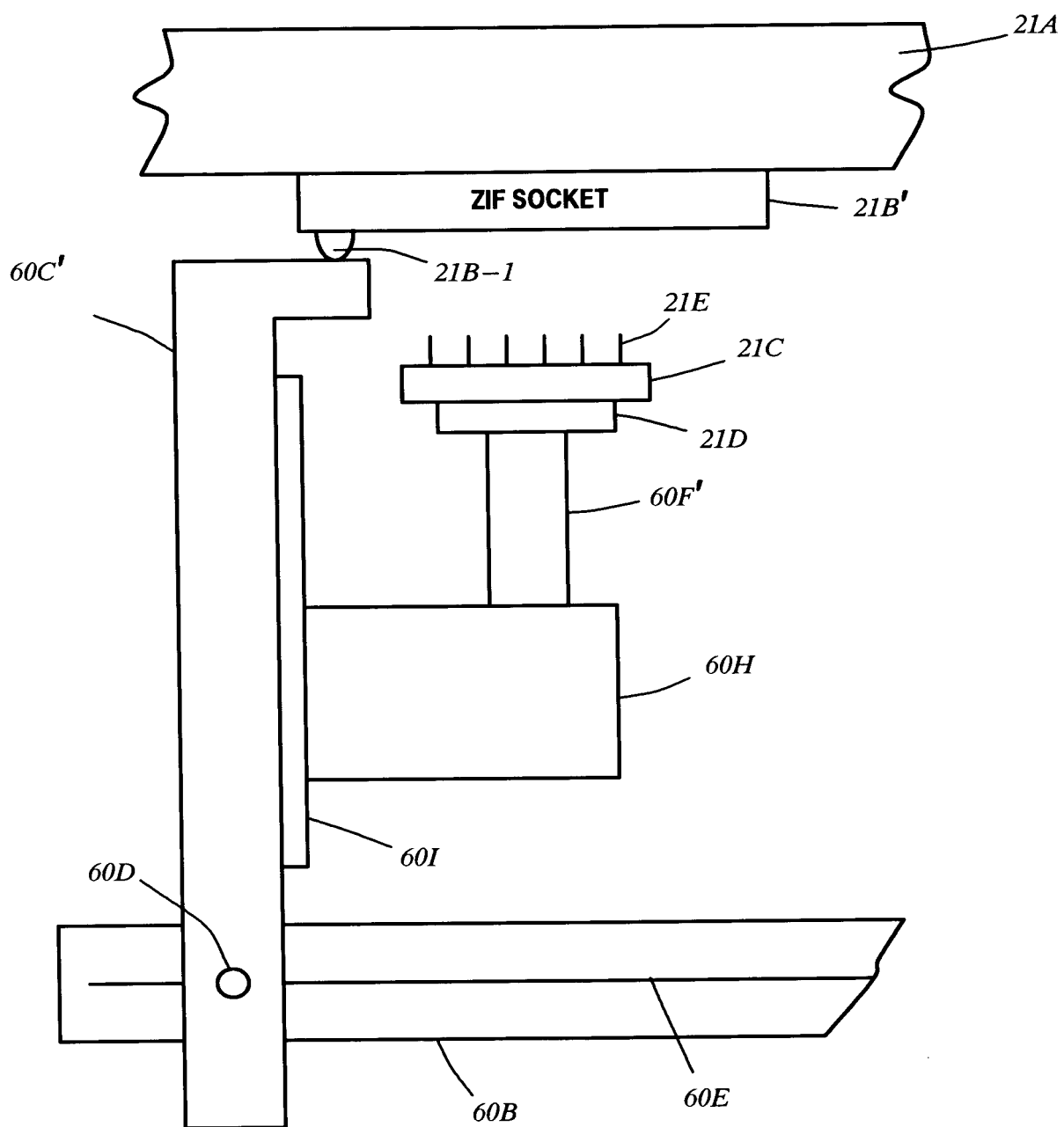
**Figure 13**



**Figure 14**



**Figure 15**



**Figure 16**